



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

LOUIS A. LIPPINCOTT

Application No.: 10/609,042

Filed: June 27, 2003

For: Memory Command Handler for Use In An

Image Signal Processor Having A Data

Driven Architecture

Art Group: 2671

Examiner: Unassigned

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

-1- 42P17010

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: October 15, 2003

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1 Examiner Name
Attorney Docket Nu

Complete if Known		
Application Number	10/609,042	
Filing Date	June 27, 2003	
First Named Inventor	Louis A. Lippincott	
Art Unit		
Examiner Name		
Attorney Docket Number	42P17010	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		INTEL CORPORATION, Intel MXP5800 Digital Media Processor, Architecture Overview, March 2003, Pages 1-38, Revision 2.1	

Examiner	Date
Signature	Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003. Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450